



Docket No.: M4065.0469/P469
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Scott Van De Graaff

Confirmation No. 6439

Application No.: 09/941,602

Group Art Unit: 2812

Filed: August 30, 2001

Examiner: Jennifer M. Kennedy

For: SMALL ANTI-FUSE CIRCUIT TO
FACILITATE PARALLEL FUSE BLOWING

#5/ELE.
10/15/2
Sunder

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
Washington, DC 20231

Dear Sir:

In response to the restriction requirement set forth in the Office Action mailed March 29, 2002 (Paper No. 4), Applicant hereby elects claims 1-28 for continued examination.

The Examiner has required restriction among:

Claims 1-28, drawn to a semiconductor device;

Claims 29-42, drawn to an apparatus used to form a semiconductor device; and

Claims 43-55, drawn to a method of making a semiconductor device.

RECEIVED
OCT -9 2002
TECHNOLOGY CENTER 2800

Application NO.: 07/71 302
Dated: April 4, 2002

DOC. NO.: M4005.0409/P409
Respectfully submitted,

By 

Thomas J. D'Amico

Registration No.: 28,371

Christopher S. Chow

Registration No.: 46,493

DICKSTEIN SHAPIRO MORIN &
OSHINSKY LLP

2101 L Street NW

Washington, DC 20037-1526

(202) 785-9700

Attorneys for Applicants